



PATENT  
8008-1052

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasutaka NAKASHIBA

Conf. 2273

Application No. 10/812,282

Group 2815

Filed March 30, 2004

Examiner J. Jackson, Jr.

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

AMENDMENT

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 8, 2007

Sir:

In response to the Official Action of August 8, 2007,  
please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing  
of claims which begins on page 2 of this paper.

**Remarks** begin on page 8 of this paper.